

ABSTRACT OF THE DISCLOSURE

A tuner circuit includes a variable attenuator circuit including a PIN diode, disposed upstream of a variable-gain amplifier circuit and controlled according to the same AGC voltage used to control the variable-gain amplifier circuit. The operations of the variable-gain amplifier circuit and the variable attenuator circuit in relation to the AGC voltage are set such that when an input RF signal is at a maximum assumed level, a direct current that flows through the PIN diode will be less than a value of a direct current that maximizes intermodulation distortion.